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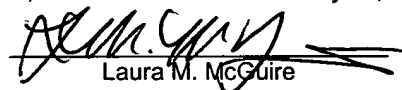
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

REPLY TO OFFICE ACTION DATED 01/09/04		Atty. Docket No. CROSS1380-1
Applicant Thomas W. Bucht		
Application Number 09/851,594	Date Filed 05/08/01	
Title Hardware Method to Reduce CPU Code Latency		
Group Art Unit 2113	Examiner Puente, Emerson C.	
Confirmation Number: 2615		

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22312-1450 on January 29, 2004.


Laura M. McGuire

Dear Sir:

In response to the Official Action mailed January 9, 2004, Applicant respectfully requests the Examiner reconsider the rejections of the Claims in view of the this reply. Please amend the application as follows:

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